

FIG.6

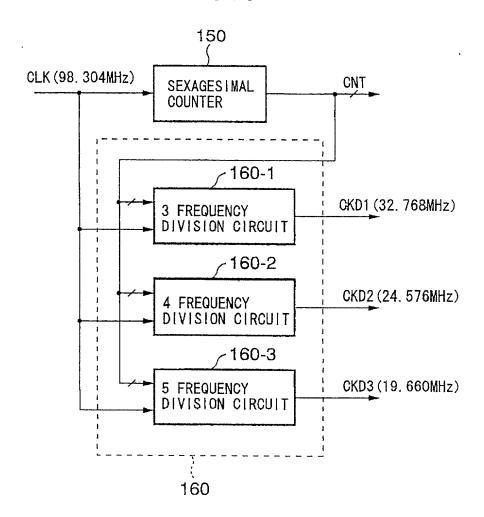


FIG.7

rs data	
CONTENTS	(MODE1)
	1 FRAME 96MS
TFPR	
NULL	

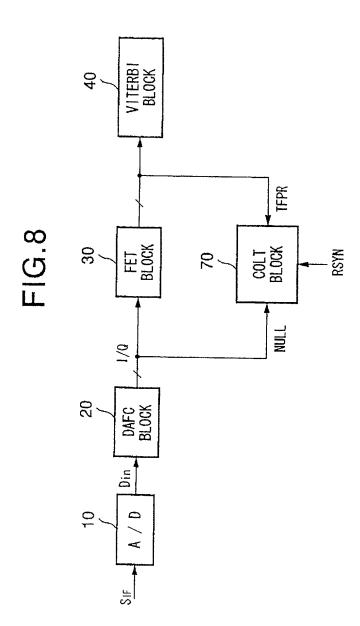
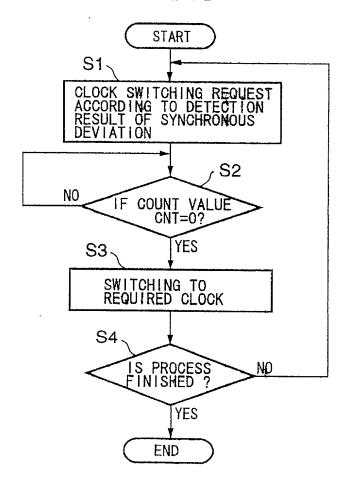
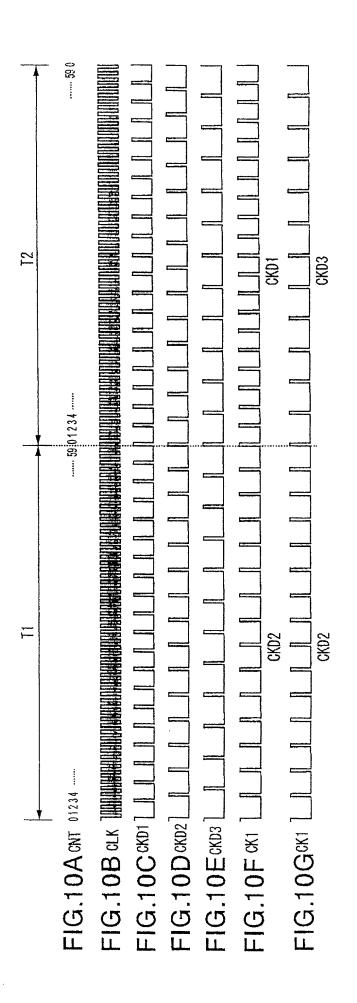


FIG.9





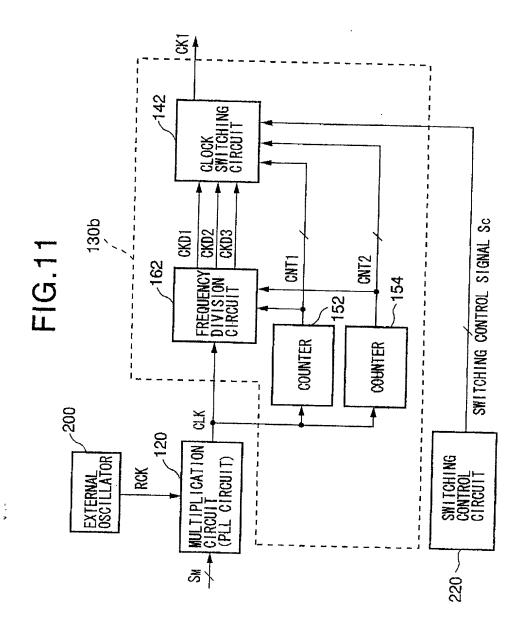


FIG.12

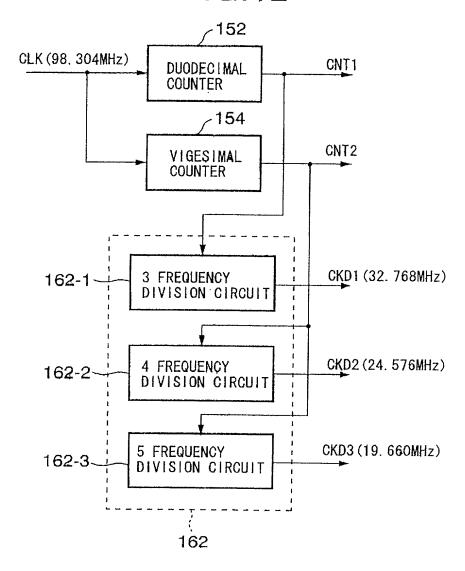


FIG.13 **START** PROVIDING INTERMEDIATE CLOCK CKD2 - SA1 CLOCK SWITCHING REQUEST ACCORDING TO DETECTION RESULT OF SYNCHRONOUS DEVIATION SA<sub>2</sub> SA3 NO CHANGE CLOCK FASTER ? SA6 YES SA4 IF COUNT VALUE CNT2=0 ? NO IF COUNT VALUE CNT1=0 ? YES SA7 YES SA5 SWITCHING TO SLOWER CLOCK CKD3 SWITCHING TO FASTER CLOCK CKD1 **END** 

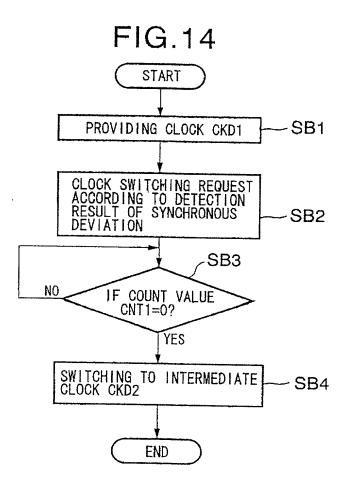
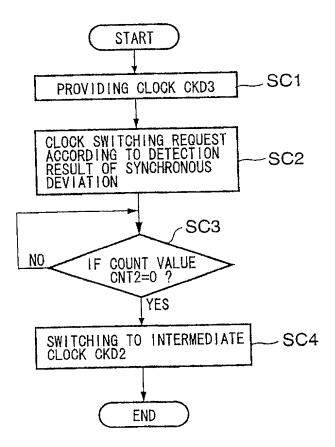
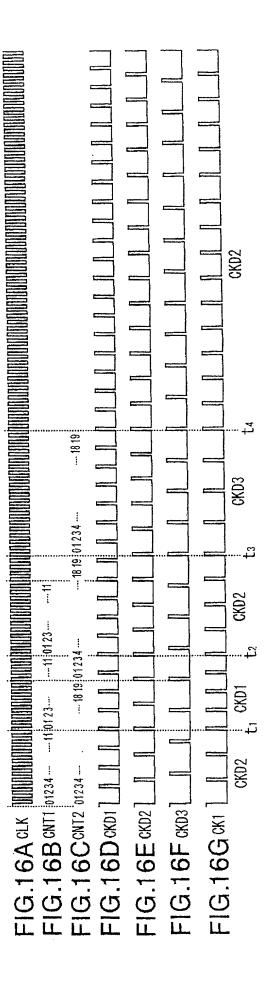


FIG.15





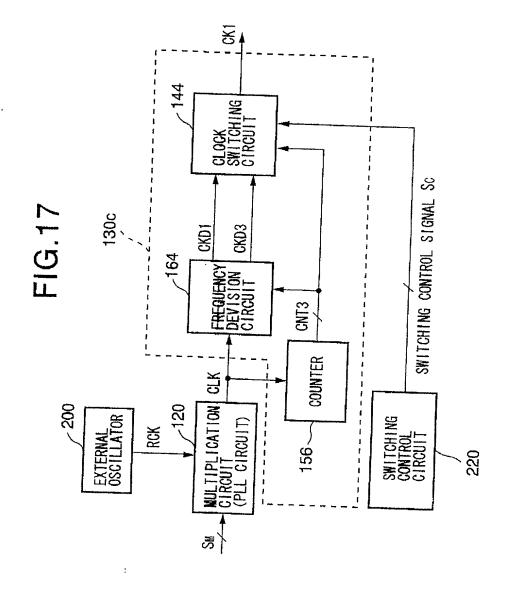


FIG.18

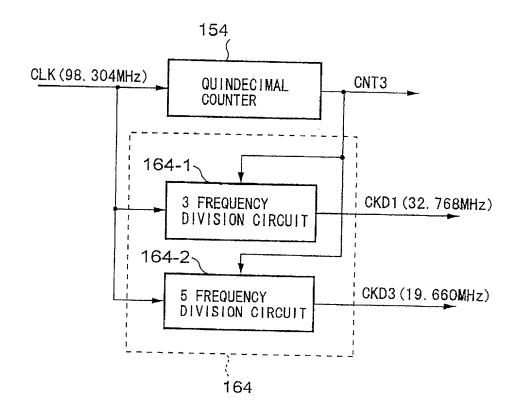


FIG.19

